

EAST - [temp.wsp:1]

File View Edit Tools Window Help

Pending

Active

- L1: (63504) bit adj (lines or line)
- L2: (6900) bitlines or bitline
- L3: (4540) digit adj (lines or line)
- L4: (1957) fold\$3 near6 (1 or 2)
- L5: (184376) arrays
- L6: (688062) array
- L7: (2378537) block
- L8: (602801) segment
- L9: (3674) subarray
- L10: (4448) sub adj 6
- L11: (754) 4 with (6 or 7 or 8 or 9 a
- L12: (39848) sense adj2 (amplifier or
- L13: (2571) shar\$3 near6 12
- L14: (171) 13 and 11
- L15: (0) 10/644610
- L34: (2629044) open or opened
- L35: (127) 34 and 14
- L36: (1140) 34 adj (1 or 2 or 3)
- L37: (2040) fold\$3 near6 (1 or 2 or 3)
- L38: (592) 36 same 37
- L39: (117295) twist
- L40: (41) 38 and 39
- L41: (4) 40 and 13
- L43: (0) 39 same 42
- L42: (121) 37 same 13

Failed

- (0) 13 same 11
- (0) 40 same 13

Saved

Search List Browse Queue

DBs USPA Plurals

Default operator: ☒ Highlight all hit terms initially

37 same 13

Fig 2

365/230.03

63

205

BR51 ISGR Image Text HTML

	U	I	Document ID	Issue Dat	Pages	Title	Current OR	Curre
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20040114	20040617	54	Content addressable mem	365/49	
2	<input type="checkbox"/>	<input type="checkbox"/>	US 20040090	20040513	281	Semiconductor memory de	365/145	257/E
3	<input type="checkbox"/>	<input type="checkbox"/>	US 20040088	20040506	11	Memory device with colu	711/105	
4	<input type="checkbox"/>	<input type="checkbox"/>	US 20040085	20040506	73	Semiconductor integrate	365/200	
5	<input type="checkbox"/>	<input type="checkbox"/>	US 20040076	20040422	12	Voltage regulator with	365/149	
6	<input type="checkbox"/>	<input type="checkbox"/>	US 20040017	20040129	41	Dynamic RAM-and semicon	365/202	257/E

Hit Details HTML

Ready

NUM

224/121